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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

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		<i>Complete if Known</i>	
Application No.	09/888,212		
Filing Date	June 22, 2001		
First Named Inventor	Kwok Hung Si		
Art Unit	2182		
Examiner Name	Yussuf Sajid		
Attorney Docket Number	20880-05629		

U.S. PATENT DOCUMENTS				
		Document No.		
Examiner Initials*	Cite No. <sup>1</sup>	Number – Kind Code <sup>2</sup> (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
<i>psl</i>	A1	US-5,515,538	05-07-1996	Kleiman
	A2	US-5,524,250	06-04-1996	Chesson et al.
	A3	US-5,727,211	03-10-1998	Gulsen
	A4	US-6,085,215	07-04-2000	Ramakrishnan et al.

FOREIGN PATENT DOCUMENTS				
		Foreign Patent Document		
Examiner Initials*	Cite No. <sup>1</sup>	Country Code <sup>3</sup> – Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
<i>psl</i>	B1	WO 99/54813	10-28-1999	Transwitch Corp.

OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS				
		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T <sup>6</sup>
<i>psl</i>	C1	EGGERS, Susan, Simultaneous Multithreading: A Platform For Next-Generation Processors, IEEE Micro, September/October 1997, pp. 12-19, 0272/1732/97.		
<i>psl</i>	C2	EL-KHARASHI, et al., Multithreaded Processors: The Upcoming Generation for Multimedia Chips, 1998 IEEE Symposium on Advances in Digital Filtering and Signal Processing, June 5-6, 1998, pp. 111-115		

Examiner Signature	<i>Yussuf Sajid</i>	Date Considered	<i>4/28/05</i>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

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